

AMENDMENTS TO THE CLAIMS

Please **AMEND** claims 23-33 as shown below.

The following is a complete list of all claims in this application.

1-22. (Currently Cancelled)

23. (Currently Amended) A liquid crystal display comprising:
a plurality of gate lines extending in a row direction and transmitting scanning signals;
a plurality of data lines extending in a column direction and transmitting picture signals;
a plurality of common storage electrode line pairs extending in the row direction, each
common storage electrode line pair comprising first and second common storage electrode lines
arranged between two neighboring gate lines;
a plurality of first and second pixels arranged alternately in the row direction, each of the
first and second pixels including a pixel electrode overlapping the storage electrode line pair a
switching element connected to a corresponding one of the gate lines and a corresponding one of
the data lines, wherein a storage capacitance is formed between the pixel electrode and the
storage electrode line corresponding thereto a liquid crystal capacitor and a storage capacitor
connected to the switching element,
wherein the storage capacitance capacitors of the first pixel is pixels are connected to
formed between the pixel electrode and the first common storage electrode lines line

corresponding thereto, and the storage capacitance capacitors of the second pixel is pixels are connected to formed between the pixel electrode and the second common storage electrode lines line corresponding thereto.

24. (Currently Amended) The liquid crystal display of claim 23, wherein the first and second common storage electrode lines transmit first and second common storage voltages, respectively,

 during a polarity of the picture signals is changed from negative to positive, the first and second common storage voltages maintain a low level when the switching elements is turned off and repeatedly swing between the low level and a high level thereafter, and

 during a polarity of the picture signals is changed from positive to negative, the first and second common storage voltages maintain the high level when the switching element is turned off and repeatedly swing between the low level to the high level thereafter.

25. (Currently Amended) The liquid crystal display of claim 24, wherein the first and second common storage voltages have inverted waveforms.

26. (Currently Amended) The liquid crystal display of claim 25, wherein the first common storage voltage applied to each first common storage electrode line is generated by inverting the first common storage voltage applied to one of the first common storage electrode lines adjacent thereto and shifting the inverted first common storage voltage by a pulse width of the scanning signals, and

the second common storage voltage applied to each second common storage electrode line is generated by inverting the second common storage voltage applied to one of the second common storage electrode line adjacent thereto and shifting the inverted second common storage voltage by a pulse width of the scanning signals.

27. (Currently Amended) A liquid crystal display comprising:

a plurality of gate lines extending in a row direction and transmitting scanning signals;

a plurality of data lines extending in a column direction and transmitting picture signals;

a plurality of common storage electrode lines extending in the column direction and transmitting common storage voltages, the common storage electrode lines and the data lines being alternately arranged; and

a plurality of pixels, each of the pixels including a pixel electrode, each storage electrode line intersecting the pixel electrodes corresponding thereto a switching element connected to one of the gate lines and one of the data lines, a liquid crystal capacitor and a storage capacitor connected to the switching element,

wherein the a storage capacitance of each pixel capacitors of the pixels is formed between the pixel electrode and the connected to the common storage electrode line corresponding thereto lines.

28. (Currently Amended) The liquid crystal display of claim 27, wherein during a polarity of the picture signals is changed from negative to positive, the common storage voltages

maintain a low level when the switching elements is turned off and repeatedly swing between the low level and a high level thereafter, and

 during a polarity of the picture signals is changed from positive to negative, the ~~common~~ storage voltages maintain a high level during the switching element is turned off and swing between the low level and the high level thereafter.

29. (Currently Amended) The liquid crystal display of claim 27, wherein the ~~common~~ storage voltages applied to the neighboring ~~common~~ storage electrode lines have inverted wave form.

30. (Currently Amended) A liquid crystal display comprising:
 a plurality of gate lines extending in a first direction and transmitting scanning signals;
 a plurality of data lines extending in a second direction and transmitting picture signals;
 a plurality of ~~common~~ storage electrode lines extending in the first direction and transmitting ~~common~~ storage voltages, the ~~common~~ storage electrode lines and the gate lines being alternately arranged; and

 a plurality of pixels arranged in a matrix, each of the pixels including a first pixel electrode overlapping the storage electrode line corresponding thereto ~~switching element connected to a corresponding one of the gate lines and a corresponding one of the data lines, a liquid crystal capacitor and a storage capacitor connected to the switching element,~~

wherein the storage capacitors capacitances of the pixels on the same row are alternately formed between the first pixel electrodes and two neighboring storage electrodes lines connected to neighboring common electrode lines.

31. (Currently Amended) The liquid crystal display of claim 30, wherein during a polarity of the picture signals is changed from negative to positive, the ~~common storage~~ voltage maintain a low level when the switching elements is turned off and repeatedly swings between the low level and a high level thereafter, and

storage voltage maintain the high level when the switching element is turned off and repeatedly swings between the low level and the high level thereafter.

32. (Currently Amended) The liquid crystal display of claim 30, wherein the ~~common~~ storage voltage applied to each ~~common~~ storage electrode line is generated by inverting the ~~common~~ storage voltage applied to one of the ~~common~~ storage electrode line adjacent thereto and shifting the inverted ~~common~~ storage voltage by a pulse width of the scanning signals.

33. (Currently Amended) The liquid crystal display of claim 30, wherein each pixel electrode further includes a second pixel electrode, and

the gate line is arranged between the first pixel electrode and the second pixel electrode

in each pixel two sub pixels arranged in the second direction, and one of the two sub pixels is

connected to the common electrode line corresponding to the pixel.